## **EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	44135	(servo adj2 motor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 15:24
L2	21650	((rotor or motor) with ((initial or start\$6) near3 position))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 15:41
L3	1107	1 and 2	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 15:25
L4	2	3 and ((adjust\$6 or chang\$6) with ((initial or start\$6) near3 position) with manual\$6) and (IC or (integrated adj circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 15:34
L5	10	3 and (digital with (IC or (integrated adj circuit)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 15:32
L6	42	3 and (voltage adj2 divider)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 15:33
L7	42	6 not 5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 15:33
L8	0	7 and (adjust\$6 or chang\$6) with ((initial or start\$6) near3 position) and (IC or (integrated adj circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 15:44
L9	29	7 and (IC or (integrated adj circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 15:41
L10	0	9 and (power near3 feedback)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 15:36
L11	0	9 and (digital with servo)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 15:37

## **EAST Search History**

		LASI Searci	,			
L12	0	9 and ((mos or mosfet or igbt or fet) near3 (serial or sequen\$8))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 15:39
L13	9020	1 and (digital or numeric)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 15:41
L14	517	13 and ((rotor or motor) with ((accelerat\$6 or initial or start\$6) near3 position))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 15:44
L15	56	14 and ((adjust\$6 or chang\$6) with position) and ((IC or (integrated adj circuit)) near3 circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 15:46
L16	45	15 and ((\$5controller or \$5processor) with output)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 15:47
L17	23	16 and divider	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 15:48
L18	15	17 and (power with feedback)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 15:51
L19	8	17 not 18	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 15:55
L20	382	((servo adj control\$6) or (servo adj motor) or (servo adj mechanism)) and feedback and (numeric\$3 or digital) and inverter and (ic or (integrated adj circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 16:03
L21	67	20 and (\$5controller or \$5processor) and ((ic or (incorporated adj circuit)) with output)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 16:07
L22	3	21 and (divider with (fet or mosfet or mos or igbt))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 16:10
L23	3	21 and (divider with (fet or mosfet or mos or igbt or inverter))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 16:07

## **EAST Search History**

L24	3	22 or 23	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 16:07
L25	1	("2003/0080772").URPN.	USPAT	OR	ON	2006/05/30 16:09
L26	45	21 and divider	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 16:11
L27	42	26 not 23	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 16:26
L28	345	318/638.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 16:26
L29	1034	318/560.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 16:26
L30	3	(28 or 29) and (ic or (integrated adj circuit)) and output and feedback and (divider with (switch\$3 or transistor or mosfet or fet or ibgt or mos))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/30 16:29